sys-sage: A Fresh View on Dynamic Topologies & Attributes of HPC Systems

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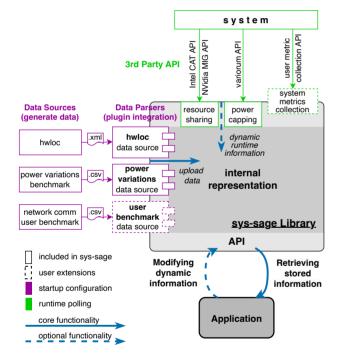
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What is sys-sage?

sys-sage is a user-side library focussing on collection, storage, and provision of arbitrary HW-relevant information within an HPC system.

sys-sage provides the needed context between the plethora of applications, tools, and benchmarks providing some information describing the increasingly complex modern HPC systems. It manages the context for both static and dynamic data, which become available at different stages of the application's lifecycle.

Internal Architecture



Internally built on two complementary structures

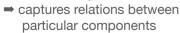
Core

CPU Socket

Component Tree

- ⇒ simple orientation
- ➡ captures hierarchy





- ➡ arbitrary connections
- ➡ non-hierarchical data

➡ highly flexible



für Bildung und Forschung

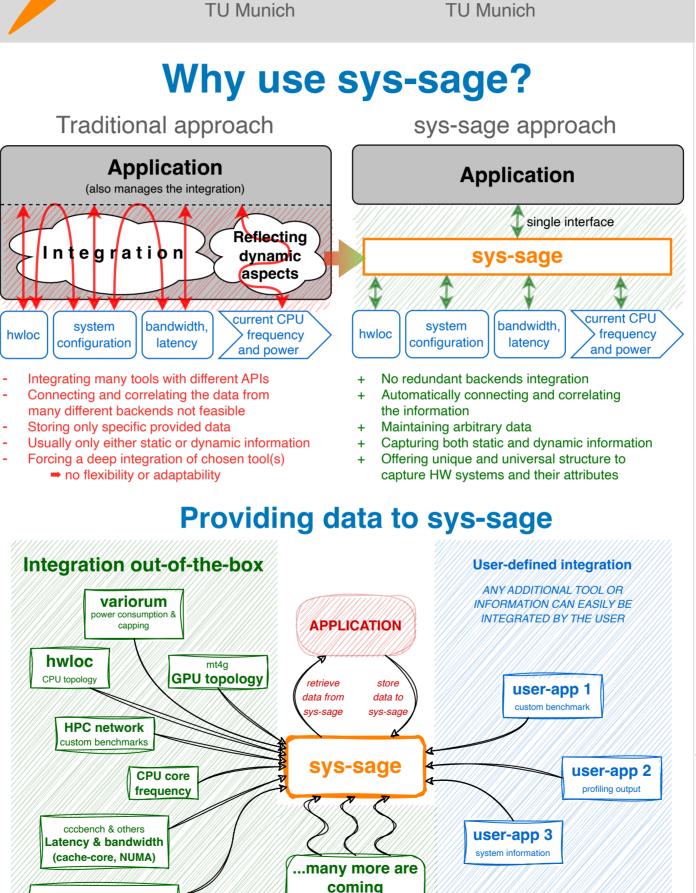
Chair of Computer Architecture and Parallel Systems, School of of Computation, Information and Technology Technical University of Munich

SM

Intel CAT & NVidia MIG

Resource sharing/isolation





PAPI counters, CPU-GPU

data transfers. I/O

characteristics.



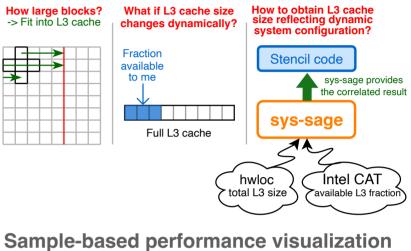
Mitos HW-related

hwloc HW informatior

Try sys-sage out and get in touch with us!

Using sys-sage

Cache-aware algorithms on systems with dynamically changing system properties



How to connect the HW-related Mitos Visualization in MemAxes samples with the hwloc HW context? code samples sys-sage contains the HW context to store & present the Mitos samples Modern HW features very

different architectures

General areas of usage:

- ✓ Code tuning
- ✓ Performance monitoring & visualization
- ✓ Performance estimation
- ✓ Performance/system modelling & simulations
- ✓ Scheduling (node- / thread-based /...)
- ✓ Co-scheduling
- ✓ Power-management
- ✓ System design
- https://github.com/caps-tum/sys-sage
- stepan.vanecek@tum.de
- spack install sys-sage



